## **SPECIFICATION AMENDMENT**

1. Please replace the paragraph beginning at line 3 page 7 with the following amended paragraph:

A THERMTRIP signal and a PROCHOT signal from each processor 104 [[is]] <u>are</u> delivered to temperature monitor 103. Temperature monitor 103 comprises logic circuitry (hardware, firmware, or instruction-based processing) that implements the functional aspects of temperature monitor 103, described below. Temperature monitor may be implemented as a programmable logic device.

2. Please replace the paragraph beginning at line 15 page 7 with the following amended paragraph:

FIGURE [[3]] 2 illustrates temperature monitor 103 and its interconnections. Nodes 101 are the same as those illustrated in FIGURE 1, each node 101 having four processors 104. The THERMTRIP and PROCHOT signal connections between processors 104 and temperature monitor 103 are direct wired connections.